

CANopen Link



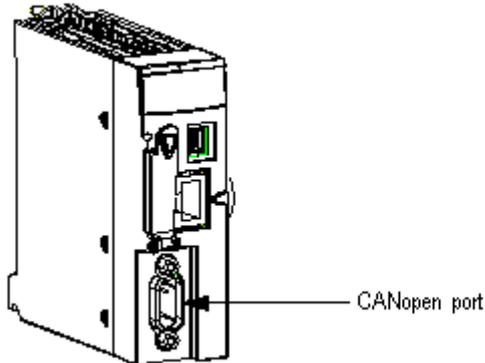
Introduction

BMX P34 2010/2030 processors support communication via a CANopen link.

These processors have a built-in communication channel dedicated to CANopen communication.

Introduction to the CANopen Port

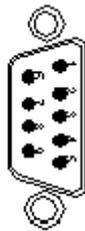
The following illustration shows the position of the BMX P34 2030 processor's CANopen port:



CANopen Connectors

The CANopen port of the processor module is fitted with a SUB-D9 connection.

The following illustration shows the processor CANopen port and the pins labels:



The following table shows the pin assignment of the CANopen link.

Pin	Signal	Description
1	-	Reserved
2	CAN_L	CAN_L bus line (low dominant)
3	CAN_GND	CAN ground
4	-	Reserved
5	Reserved	Optional CAN protection
6	(GND)	Optional ground
7	CAN_H	CAN_H bus line (high dominant)
8	-	Reserved
9	Reserved	Positive external CAN power supply (dedicated to the power supply of optocouplers and transmitters/receivers) Optional

Note: CAN_SHLD and CAN_V+ are not installed on the Modicon M340 range processors. These are reserved connections.